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A Review on Bus Interfaces of AMBA

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Abstract – Today's system-on-chip (SOC) is designed with reusable intellectual property cores to meet short time to market requirements. Embedded systems design focuses on low Power dissipation and system-on-chip. A reliable on-chip communication standard is a must in any SOC. This paper gives an informative review about the bus interfaces of Advanced Microcontroller Bus Architecture (AMBA).

Keywords – Bus Interface, AMBA.

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